CAMBRIDGE INTERNATIONAL EXAMINATIONS

GCE Advanced Level

MARK SCHEME for the May/June 2013 series

9691 COMPUTING

9691/33

Paper 3 (Written Paper), maximum raw mark 90

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This mark scheme is published as an aid to teachers and candidates, to indicate the requirements of the examination. It shows the basis on which Examiners were instructed to award marks. It does not indicate the details of the discussions that took place at an Examiners' meeting before marking began, which would have considered the acceptability of alternative answers.

Mark schemes should be read in conjunction with the question paper and the Principal Examiner Report for Teachers.

Cambridge will not enter into discussions about these mark schemes.

Cambridge is publishing the mark schemes for the May/June 2013 series for most IGCSE, GCE Advanced Level and Advanced Subsidiary Level components and some Ordinary Level components.

Page 2		Mark Scheme	Syllabus
		GCE A LEVEL – May/June 2013	9691
1	(a) (i) Man	y CLUBs play in many LEAGUEs.	Campania
	(ii) E-R	diagram	age
		CLUB Plays In LEAGUE	COM

- 1 (a) (i) Many CLUBs play in many LEAGUEs.
 - (ii) E-R diagram



(iii)



- Link table drawn [1]
- [1] - 2 x one-to-many relationships
- primary key in CLUB links to foreign key in link table [1]
- [1] - primary key in LEAGUE links to foreign key in link table

No mention of foreign keys scores max 1 for final mark points

- (b) (i) One CLUB has many PLAYERs [1]
 - [1] (ii) E-R diagram



(c) The primary key of table CLUB - ClubName [1]

Matches to ClubName in the PLAYER table [1]

(d) Displays a 'list' of the player names and registration numbers [1]

Who are female defenders [1]

[Total: 12]

2 (a) Meta language

> Rules/Grammar (which describe a high level programming language // protocol specification) The syntax or structure of all program statements [2]

- (b) (i) A rule which is defined in terms of itself NB Not 'procedure' [1]
 - (ii) Rule 6 [1]

Page 3	Mark Scheme	Syllabus	er
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Expression Valid/Invalid Ru			used	
[g]	Valid	Uses all the rules except 3		
[dc]	Invalid	- Starts with 7 - 3 not used - ends with 7		
[w,a]	Valid	Starts with 7all rules used, incl, rule 6 twice	all rules used, incl,rule 6 twiceends with 7	

(c)	<pre><doublecharacter></doublecharacter></pre>	· ::=	<character></character>	<	Character>	
	<listitem></listitem>	::= <	(Character>	•	<doublecharacter></doublecharacter>	[3]

[Total: 14]

Page 4	Mark Scheme	Syllabus	er
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- 3 (a) Indexed addressing // LDX
 - **(b)** Indirect addressing Annotation to explain that address 203 is used as a forwarding address

(c) 48 [1]

(d)

ACC			ACC IX		Output
			0		
	165				
	166				
					166
			1		
	93				
	94				
					94
L			(2)		
				l	

Mark as follows:

Index register contain 0	[1]
Sequence of first box (or subsequent sequence for the same instructions)	[1]
Index register contains 1	[1]
Sequence for final box	[1]

(e) Labels added to a (symbol) table // creates a list of addresses [1]
Labels are later looked up to determine the actual address / Assembler must allocate addresses to labels
Mnemonic looked up to give binary code/machine code [1]
Macro instructions are expanded into a group of instructions
The software makes two passes through the source program [1]
MAX 3

[Total: 11]

Page 5	Mark Scheme	Syllabus	er
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4 (a) (i) Dynamic data structure changes <u>size</u>..... At execution time

// A static data structure has a fixed size

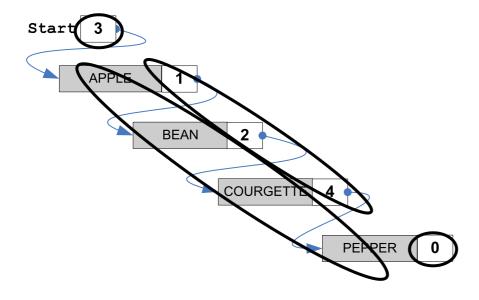
(ii)	Dynamic data structure matches size to data requirements
` '	Takes memory from heap as required //
	returns memory as required (following node delet

returns <u>memory</u> as required (following node deletion)
There is no wasted <u>memory</u> space / makes efficient use of <u>memory</u>

There is no wasted memory space / makes efficient use of memory

(b) MyList[Start].Data = APPLE
 MyList[3].Pointer = 1 [1]

(c)



[4]

[1]

[1] MAX 1

Page 6	Mark Scheme	Syllabus
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. , ,,	Traversal(MyList[Index].Pointer)	Cambridge
	tTraversal(3) List[3].Pointer <> 0 is TRUE	age.co
	ListTraversal(1)	

ListTraversal(1)

MyList[1].Pointer <> 0 is TRUE

ListTraversal(2)

MyList[2].Pointer <> 0 is TRUE

ListTraversal(4)

MyList[4].Pointer <> 0 is FALSE

OUTPUT MyList[4].Data // OUTPUT PEPPER

ENDPROCEDURE

OUTPUT COURGETTE

ENDPROCEDURE

OUTPUT BEAN

ENDPROCEDURE

OUTPUT APPLE ENDPROCEDURE

[4]

- (iii) The procedure has to backtrack/unwind from the current call [1] [1]
 - To return to the calling procedure // return to the addresses from which called

MAX 1

[Total: 15]

- 5 Interpreter translates one instruction, runs it before going on to the next // Compiler (a) – translates all the instructions before run [1]
 - Compiler creates object code/executable file // Interpreter does not [1]
 - Interpreter makes for easier debugging //compiler errors produced away from the execution [1]
 - Compiled programs will execute faster // interpreted code will execute slower [1]
 - Interpreter must be present to run the program // compiler software not needed at runtime [1]
 - Interpreter will translate code in loops more than once // Compiler only once [1]
 - Once compiled no further translation needed // Interpreter needed every program execution attempted [1]

MAX 4

Page	7	Mark Scheme	Syllabus er
	•	GCE A LEVEL – May/June 2013	9691
(b) (i)	all th The	keyword table contains: ne language keywords/reserved words + with a matching symbol table stores: each identifier/variable found (and its data type) the values of all constants the upper and lower bounds of arrays Mark as: 1 + 1	Syllabus 7.7 de er 9691 ng token [1] [1] MAX 2
(ii)	Key Lab	words are looked up in the keyword table words are converted to tokens els are looked up in the symbol table els are converted to actual addresses	[1] [1] [1] MAX 2
			[Total: 9]
(a) Ba	tch is	×	[1]
(u) Bu	1011 10	^	ניז
All Th Pro so	the (december) the (december	a time delay before processing lata) is processed together/at the same time slips are generated as a batch ng cannot start until all data has been collected/input ssing can be done on the 25 th no user involvement	[1] [1] [1] // all data entered by the 18 th [1] [1] MAX 2
Ťh	e user	ve processing is Y) continually wants to see the effect of the changes/desdata input by the user	sign produced [1] [1] MAX 1
(b) (i)	STA	FF17 can be loaded	[1]
(ii)		ition 3 is too small Il not allow all 12 students to log-on at 09:00	[1] [1]
(iii)	devi	rating system // specific modules e.g. interrupt handler ce drivers <u>mples</u> of system software or utilities	/scheduler, etc. [1] [1]
	R. "S	System software" and "Utilities"	MAX 2
(iv)		ning job currently has use of the processor	[1] [1]
	the p	pended/Blocked program is unable use the processor/ or by example, the ce s: the explanation marks are not dependant on the corr	[1]

Note: the explanation marks are not dependant on the correct name

	Dr ^	Dage 9 Mark Schome Syllahus			
	Page 8	Mark Scheme GCE A LEVEL – May/June 2013	Syllabus er 9691		
	. , . ,	ne program is divided into fixed sized units called page	es SCAM		
	P	ne memory is divided into areas (with the same size) of age management is done using a 'page frame table'	called page frames		
	A	ages will be swapped in and out of page frames as red n area on the hard disc can act as virtual memory (to	speed up the swapping) [1]		
	V	rtual memory extends memory capacity / acts as pseu	udo-memory MAX 2		
	Sa	ot all pages of the program need to loaded aves memory	[1] [1]		
	m	ore jobs can be run	[1] MAX 1		
			[Total: 16]		
7	(a) (i) Ti	rue / Yes	[1]		
	(ii) Fa	alse / No	[1]		
	. ,	OMPILE ERROR	[1]		
	(iv) C	OMPILE ERROR	[1]		
	(b)				
		CalcNetPay (EmpGrade: CHAR/STRING, Hour	sWorked:SINGLE/INTEGER) RETURN SINGLE		
	[
		[1] A : RETU	[1] JRN REAL/CURRENCY		
			[Total: 7]		
8	(a) Building a model of the system The model records over time the result of changing parameters/conditions //				
		s the behaviour of the system	[1]		
	` '	puter <u>program</u> can be written to build the model omputer system can process results very quickly / can	[1] reduce the time frame		
	Inputs	er forecasting has many changes which are based or will originate from various sensors ind speed / temperature / air pressure	n mathematical equations [1] [1] [1]		
	_				
		ful computers process results (from many sensor soul se parallel processing	rces) [1] [1]		

[Total: 6]