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CAMBRIDGE INTERNATIONAL EXAMINATIONS

GCE Advanced Subsidiary Level and GCE Advanced Level

MARK SCHEME for the May/June 2014 series

9691 COMPUTING

9691/12

Paper 1 (Written), maximum raw mark 75

This mark scheme is published as an aid to teachers and candidates, to indicate the requirements of the examination. It shows the basis on which Examiners were instructed to award marks. It does not indicate the details of the discussions that took place at an Examiners' meeting before marking began, which would have considered the acceptability of alternative answers.

Mark schemes should be read in conjunction with the question paper and the Principal Examiner Report for Teachers.

Cambridge will not enter into discussions about these mark schemes.

Cambridge is publishing the mark schemes for the May/June 2014 series for most IGCSE, GCE Advanced Level and Advanced Subsidiary Level components and some Ordinary Level components.

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1 (a)

	A	В	С	D
	primary memory storage	magnetic secondary storage	optical secondary storage	solid state secondary storage
DVD – RAM			✓	
ROM	✓			
hard disk		✓		
flash memory				✓

1 mark for each correct tick [4]

(b) (i) A [1]

(ii) B [1]

(iii) C / D (If both ringed here then still award a mark) [1]

(c) 1 mark for a benefit and 1 mark for a drawback

<u>benefit</u>

- faster start up speed (no "spin-up" required)
- no moving parts (so more robust)
- very fast read write seek (latency) times
- doesn't require additional read/write hardware devices (just plugs into USB)
- can store data indefinitely <u>provided</u> that it is periodically refreshed

drawback

- vulnerable to magnetic fields and electrostatic charges
- limited write cycles
- more vulnerable to corruption if used as primary source of saving files
- because it is small, very easy to lose

	Pa	ge 3	Mark Scheme Syllabus	er er
			GCE AS/A LEVEL – May/June 2014 9691	Do
2	(i)	Any two	from:	Cambri
		circuit sv	<u>vitching</u>	a de
		•	decided on before data transmission starts	, c.C
		•	em decides in which route to follow	ON
			and transmission goes according to this path/route	
		for v	whole length of communication session, route is dedicated exclusively	

2 (i) Any **two** from:

circuit switching

- path decided on before data transmission starts
- system decides in which route to follow ...
- ... and transmission goes according to this path/route
- for whole length of communication session, route is dedicated exclusively
- route only released when data transmission stops

packet switching

- data is broken up into packets
- packets are reassembled at destination
- packets are sent towards destination independent of each other
- each packet has to find its own route to destination
- decision as to which path to take is decided when each node is reached
- nodes are switches, routers, etc.
- each packet finds its way based on information it carries

[2]

(ii) Any **two** from:

baseband

- data sent as digital signals ...
- ... through the media as a single channel
- ... that uses entire bandwidth of the media
- it is bi-directional
- (frequency-division) multiplexing is not possible

broadband

- data sent in form of analogue signals
- each transmission is assigned to a portion of the bandwidth ...
- ... thus multiple transmissions are possible at the same time
- communication is uni-directional
- to send and receive needs two pathways ...
- ... either by assigning a frequency for sending and a different frequency for receiving
- ... or by using different communication paths
- multiplexing is possible using this method [2]

(iii) Any **two** from:

ring topology



or if diagram described

- faulty connections can cause whole network to fail
- it is difficult to expand this type of network
- works well under heavy loading
- possible to form very large networks
- no server
- less secure (because data passes through all computers)

Page 4	Mark Scheme	Syllabus
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star topolo	<u>ogy</u>	Cally
		Tabeca
	or if diagram described	OH)

star topology



- failure in any connection doesn't necessarily stop the rest of the network from working
- if the central hub/switch fails then the whole network fails
- it is easier to identify faults in this arrangement
- it is easier to expand this type of network
- needs server
- more secure (nodes contact each other directly through the hub)

[2]

- 3 (a) 1 mark for feature + 1 mark for reason (reason must match feature)
 - very clear, non-confusing icons/representations of flight paths
 - therefore it is easy to identify flights/less errors in identification
 - use of very clear and consistent colours (e.g. green ok, red danger, etc.)
 - allows "at a glance" status of each flight
 - easy to navigate between screens
 - to allow fast transfer to and from other flight information screens
 - audible warnings of dangerous situations
 - in case any messages on the screen have been "overlooked"
 - not too much information on the one screen
 - to avoid "information overload"/so that important details aren't "hidden"
 - (b) 1 mark for method + reason which must be in context of air traffic control

appropriate

- pilot or direct
- not possible to run two systems together for safety reasons
- direct is an immediate changeover, so only one system in place
- pilot system only works if there is more than one control room

not appropriate

- parallel or phased
- parallel requires two systems running concurrently .. not possible here
- whole system needs to be up and running so phased wouldn't work for safety reasons

[2]

[4]

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- (c) 1 mark for type + 1 mark for situation/description
 - adaptive ...
 - ... alter the solution to take into account changes in external influences (e.g. new airp legislation, new international safety rules, etc.)
 - perfective ...
 - ... alter the solution to improve the overall performance

[4]

Page 6	Mark So	heme	Syllabus
GC	GCE AS/A LEVEL – May/June 2014		9691
(a)			
data item	pass	fail	
K X 2 3 7 N		✓	
NA8 3KK	✓		

data item	pass	fail
K X 2 37 N		✓
NA8 3KK	✓	
W44 6PQ	✓	
C 2 4 3 3 Q		✓
R4 9NL	✓	

[5]

(b) (i)

				1	1		1
	Т	9	9	0	Т	Т	Т
	← 1 n	nark →	- ← 1 m	nark →	~	1 mark	
Alternative	Т	0	9	9	Т	Т	Т
Or	Т	9	0	9	Т	Т	т

(ii) 1 mark for correct example (allow follow through)

Examples of items that should pass:

N812 CHG B3 HMX G45 AGR

[1]

(iii) 1 mark for correct example (allow follow through)

Examples of items that should fail:

N1234 KK B202 BT 123 FKN

[1]

Page 7	Mark Scheme	Syllabus
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5 1 mark for each explanation

monitoring a chemical plant needs to be done in real time since an immediate respondentions is required (for safety reasons); batch processing would not allow this to be do in real time (OR CONVERSE ANSWER)

a programmer is still required; the costs are reduced since the development work is shared between the purchasers of the software package

it is not possible to write to ROM; so ROM cannot save data/files that are in current use; this requires RAM

sensors cannot control motors (etc.) directly; the data needs to be first read by a computer/(micro)processor which then decides what action to take based on stored data [4]

- 6 (a) a two dimensional array
 - of integer / character accept algorithm/pseudocode to initialise array / 9 × 9 grid [2]
 - (b) 1 mark per point
 - (i) using a mouse or touch screen or arrow keys ...
 - ... select square where number is to be placed
 - numbers 1–9 appear in a drop down menu/list
 - required number is selected

OR

- select square where number is to be placed
- using mouse, touch screen or arrow keys
- type in the required number using a keyboard

[2]

- (ii) Any two points from:
 - two FOR .. TO loops used to control inner and outer loops
 - computer compares each value in same position in both arrays
 - this is done for every position (81 numbers)
 - if all the numbers in both arrays are identical then puzzle is solved correctly
 - if any numbers in the two arrays are different then computer flags an error

OR

- loop to compare all cells in a row to check for duplication of numbers or blank
- loop to compare all cells in column to check for duplication of numbers or blank
- loop to compare all cells in 3 × 3 grid to check for duplication of numbers or blank [2]

	Page 8	Mark Scheme	Syllabus	er
	-	GCE AS/A LEVEL – May/June 2014	9691	6
		ach feature of the 3 types of application softw - 1 mark for output device and its justification	vare + 1 mark for input devi	dhahir
!	computer aid	led design (CAD)		36.CO
	features	3D modelling		173

computer aided design (CAD)

features

- 2D and 3D modelling
- use of wire frames
- library of parts
- ability to calculate cost of final item
- ability to calculate weight of the final object
- features such as rotation, zoom, colour, etc.
- kinematics (testing any moving parts in final design to see they interfere with each other)
- links into CAM

input device + correct justification

light pen /

graphics tablet to draw/move/select objects on the screen (if CRT

used)

trackerball to move/select items on the screen

allow users to manipulate three-dimensional space mouse

objects on a screen

as above space ball

output device + correct justification

screen needs to be large to accommodate some large monitor/screen

very complex and intricate designs

to print out large of even full size drawings (graph) plotter

3D printer to "print out" working prototypes (less expensive

than building prototype)

spreadsheet

features

- ability to do calculations
- ability to produce graphs and charts from data
- ability to use formulas
- use of macros to do automatic calculations (e.g. calculate tax) / ability to program own functions

input device + justification

keyboard to enter data into the spreadsheet directly

to select graph types/spreadsheet features from mouse

menus

output device + justification

dot matrix printer to print out the wage slips

to see graphs and charts of company sales and monitor

to produce reports for the management team and laser/inkjet printer

total wages bill for the month/year [3]

Page 9	Mark Scheme	Syllabus
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presentation features		Cambridge
	tions oultimedia (sound, movies, animation) into the prese s to websites to enhance the presentation	ntation

presentation

features

- slide transitions
- integrate multimedia (sound, movies, animation) into the presentation
- embed links to websites to enhance the presentation
- introduce attractive colours, fonts, etc. to make presentation more interesting

input device + justification

pointing device/mouse to select items for inclusion in presentation

microphone to record voice overs

keyboard to input text for use in slides to control slide show transitions remote control digital camera to input still / moving images

output device + justification

multimedia projector to allow all the features of presentation to be

shown and heard

loud speakers to hear voice overs and any background music

(etc.)

so large audiences can see presentation from large screen

anywhere in the room

to allow notes to be produced to accompany the printer

presentation

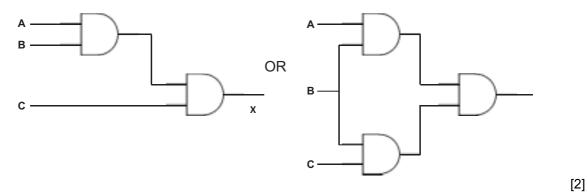
[3]

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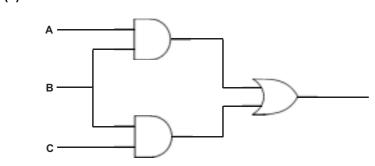
8 (a) 2 marks per part

IF candidate uses 2 gates mark from 2 gate diagram if draws 3 gates mark from 3 gate diagra 3 parts

(i) (allow correct alternatives)

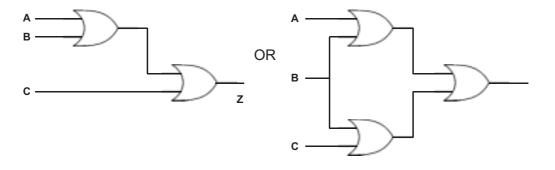






[2]

(iii) (allow correct alternatives)



[2]

Page 11		Mark Scheme				Syllabus	
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b)							Tally
	Α	В	С	Х	Υ	Z	ambridge
	0	0	0	0	0	0	
	_	0	1	0	0	1	
	U	U	· ·	U	•	•	

)						
	Α	В	С	X	Y	Z
	0	0	0	0	0	0
	0	0	1	0	0	1
	0	1	0	0	0	1
	0	1	1	0	1	1
	1	0	0	0	0	1
	1	0	1	0	0	1
	1	1	0	0	1	1
	1	1	1	1	1	1

2 marks 2 marks 2 marks

(-1 mark for each error in each column)

[6]

Page 12	Mark Scheme	Syllabus	
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9	(a)							
		0	1	1	0	1	0	0

(b) chocolate with extra milk + sugar

(c) this will cause an error message number is > max value of 63 [1]

(d) part (b): 0 part (c):

- (e) (i) Any one point from:
 - each bit in a byte is transmitted along individual channels/wires
 - each of the 8 bits/1 byte transmitted along several/8 wires/channels
 - (ii) Any three points from:

CHECK for annotations on the table of zeros and ones MARK grid first

- byte 7: 0 0 1 1 1 0 1 1 has odd parity (shown by an arrow)
- column 5 (counting from the left) indicates that parity byte is incorrect in position 5
- therefore bit in row 7, column 5 is in error
- the bit in that position should change from 1 to 0 to make even parity in all bytes
- this gives the corrected byte as: 0 0 1 1 0 0 1 1

[1]

[1]

[2]

[3]